Frank Lee

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EDUCATION

Carnegie Mellon University **Pittsburgh, Pennsylvania** **Graduation date: May 2020**
M.S. in Electrical and Computer Engineering with Concentration in Embedded Systems *GPA **3.6/4.0***

University of California, Davis **Davis, California** **Graduation date: June 2019**
B.S. in Electrical Engineering with Concentration in Analog and Digital Circuits, *GPA **3.4/4.0***

WORK EXPERIENCE

Cerebras Systems **Sunnyvale, California** **Jan 2021 – Present**
Embedded Software Engineer

- Collaborating with the hardware team to develop embedded software for server system management
- Programming real-time embedded code for STM32 microcontroller using ARM Cortex-M
- Creating communication code for IO-link sensors by reading spec sheets and implementing all communication layers from the microcontroller to sensor monitoring thread
- Utilizing oscilloscope and logic analyzer to debug communication signals
- Developing C++ code for a Linux daemon that utilizes gRPC to manage power sequencing of different boards in the system
- Simplified complex Python code into easily readable C++ code while separating testing and production code
- Mentored an intern, delegated impactful tasks, coordinated meetings, and provided regular check-ins to prevent burnout while monitoring and ensuring their success

Yinzcam **Pittsburgh, Pennsylvania** **May 2020 – July 2020**
Embedded Systems Engineer Intern / Hardware and Firmware lead (Athletech group)

- Designed and created a proof-of-concept board capable of real-time biophysical trait measurement for athlete performance monitoring
- Developed a custom PCB using Autodesk Eagle software, incorporating a microcontroller, battery circuits, and 5-6 sensors, while meeting high-level functional requirements
- Configured pins on an ARM-based microcontroller to match required peripheral specifications, and wrote industry-standard embedded C firmware code using a modern IDE

Texas Instruments **Santa Clara, California** **June 2019 – August 2019**
Digital Design Engineer Intern (High Speed Signal Conditioning group)

- Designed and verified Verilog RTL code for integration with TI's latest PCIe Retimer chip

RMI Institute **Davis, California** **June 2018 – September 2018**
Electrical Engineering Intern

- Designed and developed an embedded system that converts digital signals from industrial pressure sensors into a data server, complete with visual displays

OSIsoft **San Leandro, California** **June 2017 – September 2017**
Customer Support Engineer Intern

- Developed a system that monitors and visualizes engine and GPS data for buses, utilizing innovative device connections to prevent accidents during its one-year operation
- Presented the project at the OSIsoft PI World Conference 2018 Academic Symposium as an invited speaker (Link to the video)

SKILLS

- Programming: C/C++, Embedded C, Python, Linux
- Communication Protocols: I2C, SPI, UART, IO-link
- Software Applications: Git, Jira, VS Code
- Characteristics: Fast Learner, Punctual, Determined, Curious, Organized, Helpful, Patient